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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Nandakumar
Serial No.: 09/876,292
Filed: 06/07/01
For:

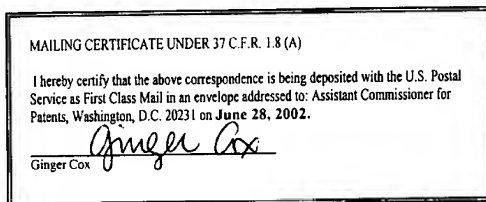
Art Unit: 2815
Examiner: Edgardo Ortiz
Docket: TI-31089

ADDITIONAL n-TYPE LDD/POCKET IMPLANT FOR IMPROVING SHORT-CHANNEL NMOS ESD ROBUSTNESS

ELECTION

June 28, 2002

Assistant Commissioner
for Patents
Washington, D.C. 20231



Sir:

Responsive to the Office Action dated June 4, 2002, please enter the following:

For prosecution, Applicants elect Group I, claims 1-11, drawn to an integrated circuit, classified in class 257, subclass 344.

Applicants acknowledge that claims 12-26 are withdrawn from further examination.

A prompt action on the merits is respectfully requested.

Respectfully Submitted,

Gary C. Honeycutt
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Attorney for Applicant

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June 28, 2002

VIA FIRST CLASS MAIL

Assistant Commissioner for Patents
Washington, D.C. 20231

Re: Patent Application For:
**"ADDITIONAL N-TYPE LDD/POCKET IMPLANT FOR IMPROVING
SHORT-CHANNEL NMOS ESD ROBUSTNESS"**
Serial No.: 09/876,292
Attorney Docket No.: TI-31089
Our File: 50000.2138

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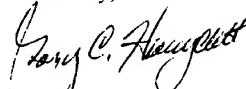
Dear Sir:

Enclosed for filing please find the following items relating to the above-identified application:

- (1) Election Document; and
- (2) Postcard.

Please file the above documents and return the date-stamped postcard to the corresponding addresses as indicated. In the meantime, if you have any questions or comments concerning this matter, please call the undersigned. Otherwise, please accept the enclosed.

Sincerely,


Gary C. Honeycutt
Reg. No. 20,250

GCH:glc
Enclosures